

Low Fuse

7	6	5	4	3	2	1	0
CKDIV8	CKOUT	SUT1	SUT0	CKSEL3	CKSEL2	CKSEL1	CKSEL0
0 prog.	1 unprog	1 unprog	0 prog	0 prog	1 unprog	0 prog	0 prog
<i>Clock /8 en.</i>	<i>/Output Clock</i>	<i>Maximum startup time</i>		<i>Internal RC oscillator @ 8Mhz</i>			

				0	0	0	0	External Clock
				0	0	1	0	Internal RC 4MHz
				0	1	0	0	Internal RC 8MHz
				0	1	1	0	Watchdog 128kHz
	0	0	1	0	0	0	0	Ceramic 0.4-0.9 Mhz 258 CK fast
	0	1	1	0	1	0	0	0.9 – 3.0 Mhz 258 CK slow
	1	0	1	1	1	0	0	3.0 – 8.0 Mhz 1K CK bod
	1	1	1	1	1	1	0	8.0 – ... Mhz 1K CK fast
	0	0	1	0	0	0	1	Ceramic 0.4-0.9 Mhz 1K CK slow
	0	1	1	0	1	1	1	0.9 – 3.0 Mhz 16 CK cryst. bod
	1	0	1	1	1	0	1	3.0 – 8.0 Mhz 16 CK cryst. fast
	1	1	1	1	1	1	1	8.0 – ... Mhz 16 CK cryst. slow

High Fuse

7	6	5	4	3	2	1	0
DWEN	EESAVE	SPIEN	WDTON	BODLVL2	BODLVL1	BODLVL0	RSTDISBL
1 unprog	1 unprog	0 prog.	1 unprog	1 unprog	1 unprog	1 unprog	1 unprog
<i>Debug Disabl.</i>	<i>eeeprom RAZ</i>	<i>SPI Enable</i>	<i>No WD</i>	<i>Brown out no</i>	<i>Brown out no</i>	<i>Brown out no</i>	<i>Ext. Rst Enable</i>

			1	1	1	BOD disable
			1	1	0	1.8 Volts
			1	0	1	2.7 Volts
			1	0	0	4.3 Volts
